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Scott Hauck, Stephen Knol

 May 1998 [DAC '98: Proceedings of the 35th annual conference on Design automation](#)

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Internet-based computing has significant potential for improving most high-performance compu including VLSI CAD. In this paper we consider the ramifications of the Internet on electronics de and develop two models for Web-based CAD. We also ...

Keywords: Internet, Web-based CAD, data security, encryption

3 [A survey and analysis of Electronic Healthcare Record standards](#)

Marco Eichelberg, Thomas Aden, Jörg Riesmeier, Asuman Dogac, Gokce B. Laleci

 December 2005 [ACM Computing Surveys \(CSUR\)](#), [Volume 37 Issue 4](#)

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Medical information systems today store clinical information about patients in all kinds of propri formats. To address the resulting interoperability problems, several Electronic Healthcare Recor standards that structure the clinical content for ...

Keywords: Electronic Healthcare Record standards, eHealth, interoperability

4 [Post-placement C-slow retiming for the xilinx virtex FPGA](#)

-  Nicholas Weaver, Yury Markovskiy, Yatish Patel, John Wawrzynek
February 2003 FPGA '03: Proceedings of the 2003 ACM/SIGDA eleventh international symposium on programmable gate arrays

Publisher: ACM

Full text available:  pdf(222.65 KB)

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C-slow retiming is a process of automatically increasing the throughput of a design by enabling grained pipelining of problems with feedback loops. This transformation is especially appropriate applied to FPGA designs because of the large number ...

Keywords: C-slow retiming, FPGA CAD, FPGA optimization, retiming

5 Cryptographic rights management of FPGA intellectual property cores

-  Tom Kean
February 2002 FPGA '02: Proceedings of the 2002 ACM/SIGDA tenth international symposium on F programmable gate arrays

Publisher: ACM

Full text available:  pdf(171.79 KB)

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As the capacity of FPGA's increases to millions of equivalent gates the use of Intellectual Property cores becomes increasingly important to control design complexity. FPGA's are becoming platform for integrating a system solution from components ...

Keywords: FPGA, cryptography, intellectual property, rights management

6 A hierarchical modeling framework for on-chip communication architectures of multiprocessor SoCs

-  Xinping Zhu, Sharad Malik
January 2007 ACM Transactions on Design Automation of Electronic Systems (TODAES), Volume 12 Issue 1

Publisher: ACM

Full text available:  pdf(441.03 KB)

Additional Information: full citation, abstract, references, index terms





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In multiprocessor-based SoCs, optimizing the communication architecture is often as important, more important, than optimizing the computation architecture. While there are mature platform techniques for the modeling and evaluation of architectures ...

Keywords: Retargetable simulation, bus, design exploration, multiprocessor system, network-on-chip, object-oriented modeling, on-chip communication architecture, packet-switching network

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